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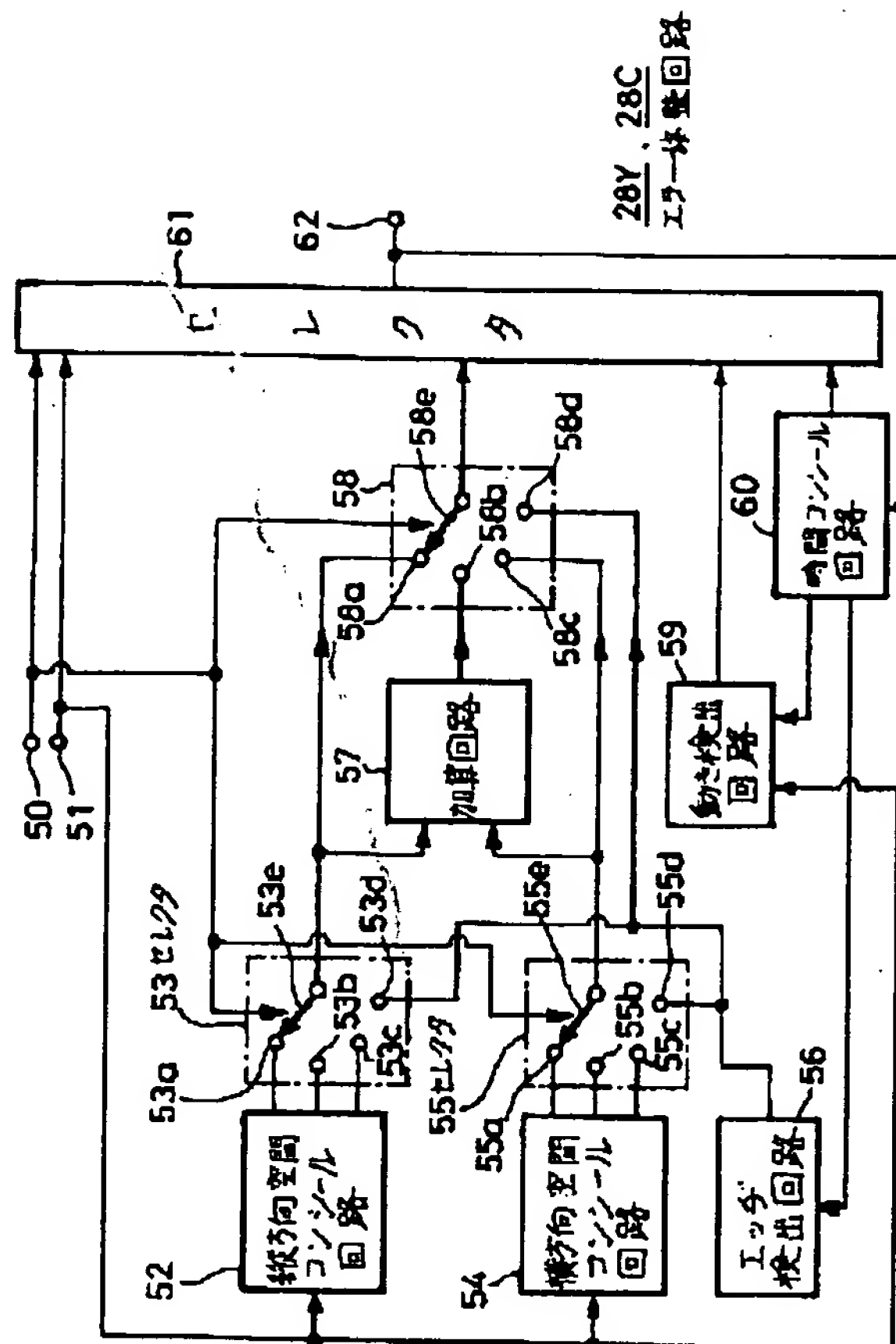
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TITLE : ERROR RECOVERY CIRCUIT



ABSTRACT : PURPOSE: To constitute a digital VTR where the picture quality is high and pictures are recorded for a long time by selectively outputting plural interpolation value data from a correcting circuit based on the detection result of an edge detecting circuit and an error signal to minimize the degradation of the picture quality at the time of the occurrence of error.

CONSTITUTION: Vertical-direction and horizontal-direction space conceal circuits 52 and 54 and a time conceal circuit 60 perform the product sum processing of reproduced video data to obtain plural interpolation value data related to data correlations. An edge detecting circuit 56 detects edges based on respective video data of circuits 52 and 54 and the output from the time conceal circuit 60 and supplies the detection output to selectors 53, 55, and 58. Selectors 53, 55, 58, and 61 and an adding circuit 57 selectively output plural interpolation value data from vertical-direction and horizontal-direction conceal circuits 52 and 54 and the time conceal circuit 60 based on the detection result from the edge detecting circuit and an error flag.

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